

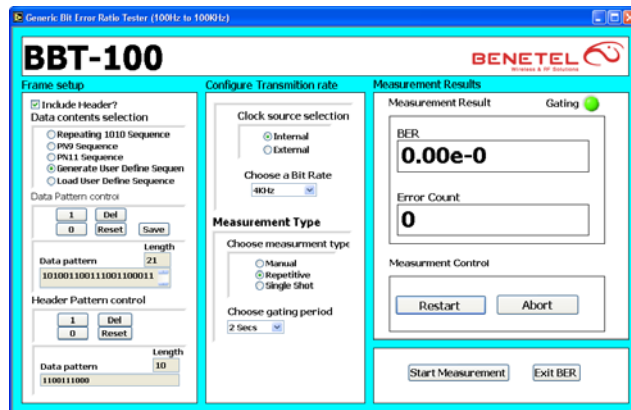
BBT-100 Bit Error Rate Tester

Features

- Up to 100kHz Clock Rate
- Selectable Internal/External Clock
- Windows™ Software Interface
- 5 Data Setup Options
- 2 pseudo-random patterns
- 3 Gating Types
- Frame Header Generation
- Frame Length 10 to 2500 Bits
- Gate period 1 sec to infinity
- TTL Interface

Typical Applications

- Communication Link Testing
- Keyless Entry Device Testing
- Telemetry Device Testing



BBT-100 Software GUI

Overview

The BBT-100 is a low cost but extremely versatile Bit Error Ratio Tester for applications requiring BER measurements up to 100kBs. It is suitable for both design verification and manufacturing test environments. It is also a useful tool for verifying communication links at installation time. The BBT-100 is comprised of a software module and a PCI plug in card. These are connected to the device under test using a breakout box. The BBT-100 is configured via an easy to use intuitive GUI. All transmit and receive parameters are set using this GUI. A unique feature of the BBT-100 is that it allows the user the option of outputting the data as frames with headers. The content and length of the header can be defined by the user. The BBT-100 scans the received data for the header and then calculates the BER of the data in the received frame.

Transmitter

The BBT-100 transmitter generates the user selected pattern for input to the device under test. The user can select to add a header of up to 30 bits to the frame to be generated. Use of a header is optional. The user can also select the frame length to a maximum of 2500 bits. The data to be transmitted is user selectable from the following options.

- Repeating 1010
- PN9 Data
- PN15 Data
- Generate User Data
- Load User Data

Receiver

The BBT-100 receiver clocks the data into a receiver data buffer. If a header has been selected it will scan the data to find the header and a valid frame. Or it will synchronize to the data the user has selected to be transmitted. From the data it counts the number of bits in error

Hardware

The BBT-100 runs on a PC with a plug-in high speed DIO board. A breakout box is supplied as part of the BBT-100. The breakout box is fitted with the following connectors

- TX Data Output
- TX Clock Output
- RX Data Input
- RX Clock Input
- External Clock Input

Specifications

General

- 100Hz to 100kHz Clock Rate
- TTL Interfaces
- BNC Connectors

Data Setup Options

- Repeating 1010 sequence
- PN9 sequence
- PN11 sequence
- Generate User sequence
- Load User sequence

Header

- Length 30 bits max

Software

The Graphical User Interface provides the user with an easy to use method of setting up transmitter and receiver parameters. The Bit Error Rate and Bit Error Count are displayed in real time. The user can Start, Stop, Abort or Reset the measurement. Using two BBT-100 setups allows for testing of communication links where the devices under test, transmitters and receivers, are not located in the same area.

Frame

- Max Length 2500 bits
- Min Length 10 bits
- PN9 Length set to 511
- PN11 Length set to 2047

Gating Period

- 1 Second to Infinity
 - Manual
 - Periodic
 - Single Shot

Measurement

- Bit Error Rate
- Bit Error Count

To Find Out More:

To order the BBT-100 or get more information, please email us at sales@benetel.com or phone us on +353 1 4100947 or browse our website at www.benetel.com

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